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APPLICATION NO.	FILING DATE		FIRST NAMED INVENTO		$\overline{+}$		DOCKET NO.	CONFIRMATION NO.
10/722,814	<u> </u>		Cha Deok Dong	· \ -/ · · · · · ·			<u>_</u>	9355
10/722,814 11/26/2003 Cha Deok Dong 29936/39765 9355 TITLE OF INVENTION: METHOD OF FORMING GATE ELECTRODE IN SEMICONDUCTOR DEVICE								
				61	/09/268	7 EHAILE2	02000332 10	3722814
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DU	E PREV. PA	DISSUE	ÉE TO	AL FEE(S) DUE	1400.00 07 300 04 EDUE
nonprovisional	NO	\$1400	\$300	93 93	\$0C.800	i	\$1700	02/26/2007
EXAM	INER	ART UNIT	CLASS-SUBCLASS					
DEO, DUY VU NGUYEN		1765	438-257000					
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` Hynix Semic	conductor Inc.	Kyungki-do, Republic of Korea						
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